

Excellent Integrated System Limited

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<u>Vishay Semiconductor/Diodes Division</u> <u>LS103A-GS18</u>

For any questions, you can email us directly: sales@integrated-circuit.com

Distributor of Vishay Semiconductor/Diodes Division: Excellent Integrated System Limite

Datasheet of LS103A-GS18 - DIODE SCHOTTKY 40V 15A SOD80

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www.vishay.com

LS103A, LS103B, LS103C

Vishay Semiconductors

Small Signal Schottky Diodes

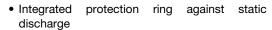


MECHANICAL DATA

Case: QuadroMELF SOD-80
Weight: approx. 34 mg
Cathode band color: black
Packaging codes/options:

GS18/10K per 13" reel (8 mm tape), 10K/box GS08/2.5K per 7" reel (8 mm tape), 12.5K/box

FEATURES





· Low leakage current

Low forward voltage drop

• AEC-Q101 qualified

 Material categorization: For definitions of compliance please see www.vishay.com/doc?99912



ROHS

APPLICATIONS

- HF-detector
- Protection circuit
- · Small battery charger
- AC-DC/DC-DC converter for notebooks

PARTS TABLE					
PART	TYPE DIFFERENTATION	TYPE DIFFERENTATION ORDERING CODE INTERNAL CONSTRUCTION		REMARKS	
LS103A	V _R = 40 V	LS103A-GS18 or LS103A-GS08	Single diode	Tape and reel	
LS103B	V _R = 30 V	LS103B-GS18 or LS103B-GS08	Single diode	Tape and reel	
LS103C	V _R = 20 V	LS103C-GS18 or LS103C-GS08	Single diode	Tape and reel	

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	PART	SYMBOL	VALUE	UNIT	
		LS103A	V _R	40	V	
Reverse voltage		LS103B	V_R	30	V	
		LS103C	V_R	20	V	
Peak forward surge current $t_p = 300 \mu s$, square pulse			I _{FSM}	15	А	
Power dissipation			P _{tot}	400	mW	

THERMAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)					
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT	
Thermal resistance junction to ambient air	On PC board 50 mm x 50 mm x 1.6 mm	R _{thJA}	250	K/W	
Junction temperature		T _j	125	°C	
Storage temperature range		T _{stg}	- 65 to + 150	°C	

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ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	SYMBOL	MIN.	TYP.	MAX.	UNIT
Reverse breakdown voltage	Ι _R = 10 μΑ	LS103A	V _(BR)	40			V
		LS103B	V _(BR)	30			V
		LS103C	V _(BR)	20			V
Leakage current	V _R = 30 V	LS103A	I _R			5	μA
	V _R = 20 V	LS103B	I _R			5	μΑ
	V _R = 10 V	LS103C	I _R			5	μΑ
Forward voltage drop	I _F = 20 mA		V _F			370	mV
	I _F = 200 mA		V _F			600	mV
Diode capacitance	V _R = 0 V, f = 1 MHz		C _D		50		pF
Reverse recovery time	$I_F = I_R = 50 \text{ mA to } 200 \text{ mA},$ recover to 0.1 I_R		t _{rr}		10		ns

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

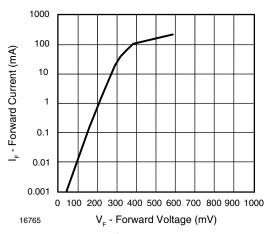


Fig. 1 - Forward Current vs. Forward Voltage

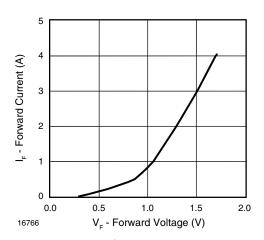


Fig. 2 - Forward Current vs. Forward Voltage

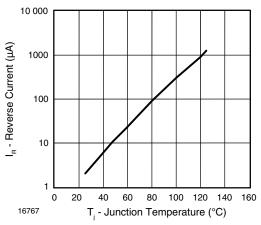


Fig. 3 - Reverse Current vs. Junction Temperature

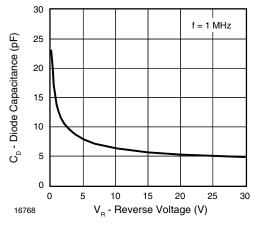


Fig. 4 - Diode Capacitance vs. Reverse Voltage

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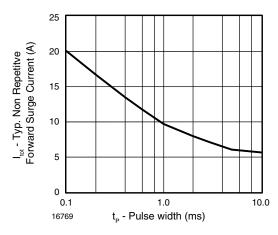
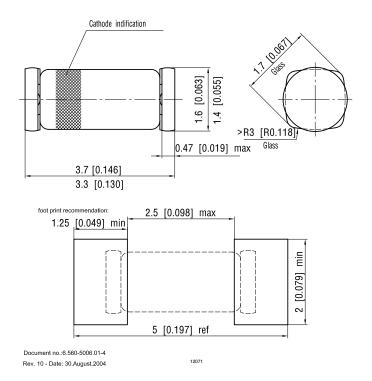


Fig. 5 - Typical Non-Repetitive Forward Surge Current vs. Pulse Width

PACKAGE DIMENSIONS in millimeters (inches): QuadroMELF SOD-80





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