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Fairchild Semiconductor FDS3680

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SEMICONDUCTOR TM

January 2000 PRELIMINARY

FDS3680

100V N-Channel PowerTrench® MOSFET

General Description

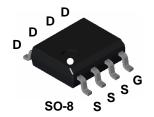
This N-Channel MOSFET has been designed specifically to improve the overall efficiency of DC/DC converters using either synchronous or conventional switching PWM controllers.

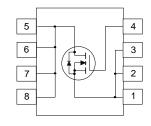
These MOSFETs feature faster switching and lower gate charge than other MOSFETs with comparable $R_{\text{DS(ON)}}$ specifications.

The result is a MOSFET that is easy and safer to drive (even at very high frequencies), and DC/DC power supply designs with higher overall efficiency.

Features

- 5.2 A, 100 V. $R_{DS(ON)} = 0.043~\Omega~@V_{GS} = 10~V$ $R_{DS(ON)} = 0.048~\Omega~@V_{GS} = 6~V.$
- · Low gate charge.
- · Fast switching speed
- High performance trench technology for extremely low $R_{\text{DS}(\text{ON})}$.
- · High power and current handling capability.





Absolute Maximum Ratings TA=25°C unless otherwise noted

Symbol	Parameter		Ratings	Units
V _{DSS}	Drain-Source Voltage		100	V
V _{GSS}	Gate-Source Voltage		±20	V
I _D	Drain Current - Continuous	(Note 1a)	5.2	А
	– Pulsed		50	
P _D	Power Dissipation for Single Operation	(Note 1a)	2.5	W
		(Note 1b)	1.2	
		(Note 1c)	1.0	
T _J , T _{STG}	Operating and Storage Junction Temperature Range		-55 to +150	°C

Thermal Characteristics

$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	(Note 1a)	50	°C/W	
R _{θJC}	Thermal Resistance, Junction-to-Case	(Note 1)	25	°C/W	

Package Marking and Ordering Information

Device Marking	Device	Reel Size	Tape width	Quantity	
FDS3680	FDS3680 FDS3680 13"		12mm	2500 units	

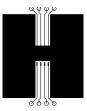
©1999 Fairchild Semiconductor Corporation FDS3680 Rev B1 (W)



Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
Off Char	acteristics		•	•	•	
BV _{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	100			V
<u>ΔBVdss</u> ΔT _J	Breakdown Voltage Temperature Coefficient	$I_D = 250 \mu\text{A}, \text{Referenced to } 25^{\circ}\text{C}$		101		mV/°C
I _{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 80 \text{ V}, \qquad V_{GS} = 0 \text{ V}$			25	μΑ
I _{GSSF}	Gate–Body Leakage Current, Forward	$V_{GS} = 20 \text{ V}, \qquad V_{DS} = 0 \text{ V}$			100	nA
I _{GSSR}	Gate–Body Leakage Current, Reverse	$V_{GS} = -20 \text{ V}$ $V_{DS} = 0 \text{ V}$			-100	nA
On Char	acteristics (Note 2)					
V _{GS(th)}	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250 \mu\text{A}$	2	2.4	4	V
$\Delta V_{GS(th)} \over \Delta T_J$	Gate Threshold Voltage Temperature Coefficient	I_D = 250 μ A,Referenced to 25°C		-6.5		mV/°C
R _{DS(on)}	Static Drain–Source On–Resistance	$V_{GS} = 10 \text{ V}, \qquad I_D = 5.2 \text{ A} $ $V_{GS} = 10 \text{ V}, \qquad I_D = 5.2 \text{ A} $ $T_{ij} = 125^{\circ}\text{C}$		0.032 0.061	0.043 0.086	Ω
		$V_{GS} = 6 \text{ V}, \qquad I_{D} = 4.5 \text{ A}$		0.034	0.048	
I _{D(on)}	On–State Drain Current	$V_{GS} = 10 \text{ V}, \qquad V_{DS} = 5 \text{ V}$	25			Α
g _{FS}	Forward Transconductance	$V_{DS} = 5 V$, $I_{D} = 5.2 A$		25		S
Dynamic	Characteristics					
C _{iss}	Input Capacitance	$V_{DS} = 50 \text{ V}, \qquad V_{GS} = 0 \text{ V},$		1735		pF
Coss	Output Capacitance	f = 1.0 MHz		176		pF
C _{rss}	Reverse Transfer Capacitance	7		53		pF
Switchir	ng Characteristics (Note 2)					
t _{d(on)}	Turn-On Delay Time	$V_{DD} = 50 \text{ V}, \qquad I_{D} = 1 \text{ A},$		14	25	ns
t _r	Turn-On Rise Time	$V_{GS} = 10 \text{ V}, \qquad R_{GEN} = 10 \Omega$		8.5	17	ns
t _{d(off)}	Turn-Off Delay Time			63	94	ns
t _f	Turn-Off Fall Time	7		21	34	ns
Q _g	Total Gate Charge	$V_{DS} = 50 \text{ V}, \qquad I_{D} = 6 \text{ A},$		38	53	nC
Q_{gs}	Gate-Source Charge	V _{GS} = 10 V		8.1		nC
Q_{gd}	Gate-Drain Charge	7		9.2		nC
Drain-S	ource Diode Characteristics	and Maximum Ratings	•	•	•	
I _s	Maximum Continuous Drain–Source				2.1	Α
V _{SD}	Drain-Source Diode Forward Voltage	$V_{GS} = 0 \text{ V}, I_{S} = 2.1 \text{ A}$ (Note 2)		0.73	1.2	V

Notes:

 R_{8JA} is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. R_{8JC} is guaranteed by design while R_{8CA} is determined by the user's board design.



a) 50°C/W when mounted on a 1in² pad of 2 oz copper



b) 105°C/W when mounted on a 0.04 in² pad of 2 oz copper



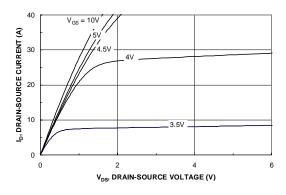
c) 125°C/W when mounted on a minimum pad.

Scale 1 : 1 on letter size paper

2. Pulse Test: Pulse Width < 300μ s, Duty Cycle < 2.0%



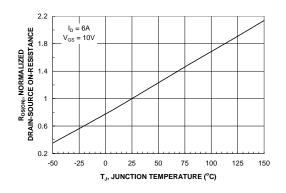
Typical Characteristics



0.8 0 10 20 30 40 50 60 10 DRAIN CURRENT (A)

Figure 1. On-Region Characteristics.

Figure 2. On-Resistance Variation with Drain Current and Gate Voltage.



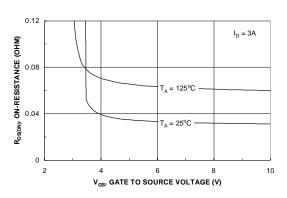
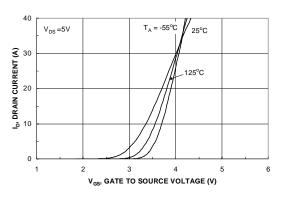


Figure 3. On-Resistance Variation withTemperature.

Figure 4. On-Resistance Variation with Gate-to-Source Voltage.



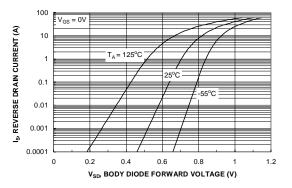
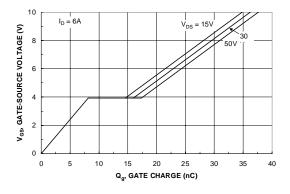


Figure 5. Transfer Characteristics.

Figure 6. Body Diode Forward Voltage Variation with Source Current and Temperature.



Typical Characteristics



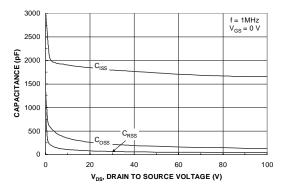
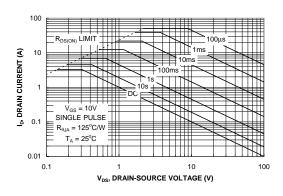


Figure 7. Gate Charge Characteristics.

Figure 8. Capacitance Characteristics.



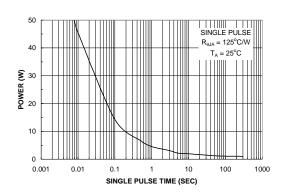


Figure 9. Maximum Safe Operating Area.

Figure 10. Single Pulse Maximum Power Dissipation.

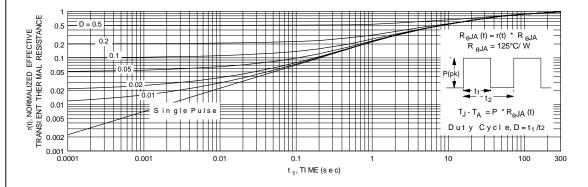
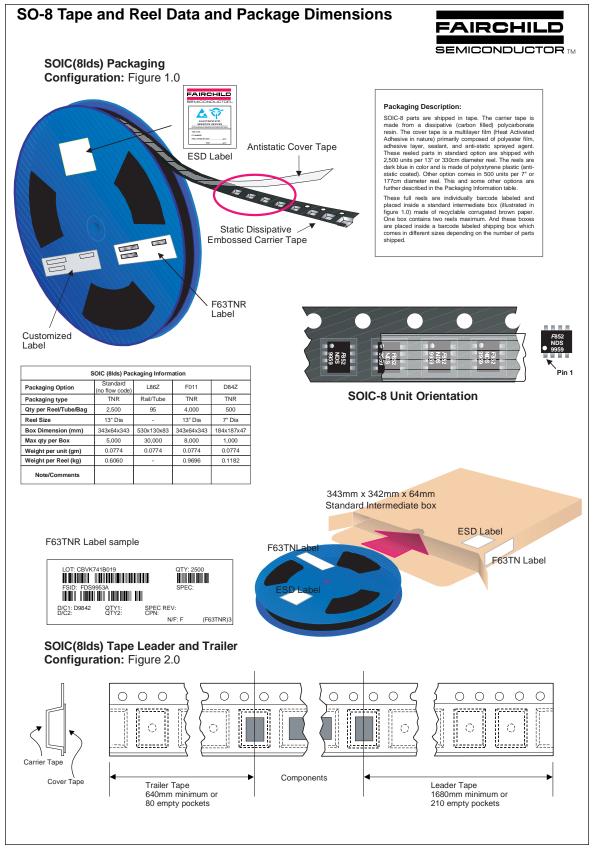


Figure 11. Transient Thermal Response Curve.

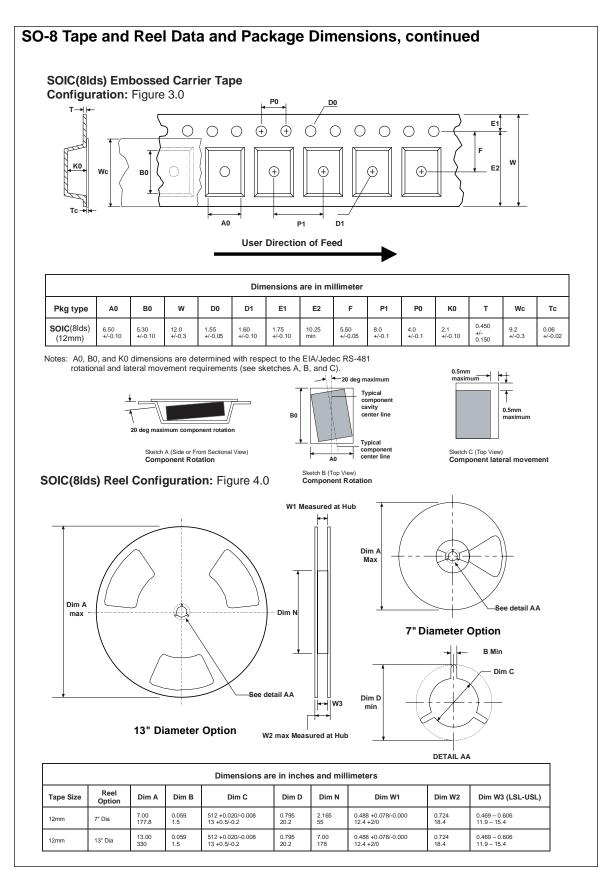
Thermal characterization performed using the conditions described in Note 1c. Transient themal response will change depending on the circuit board design.

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Datasheet of FDS3680 - MOSFET N-CH 100V 5.2A 8-SOIC Contact us: sales@integrated-circuit.com Website: www.integrated-circuit.com



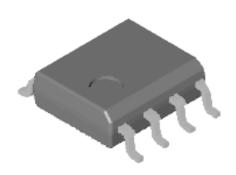
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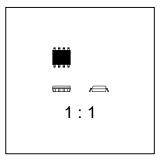




SO-8 Tape and Reel Data and Package Dimensions, continued

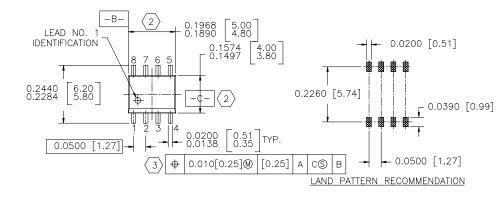
SOIC-8 (FS PKG Code S1)

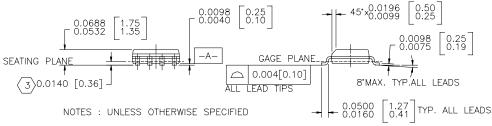




Scale 1:1 on letter size paper
Dimensions shown below are in:
inches [millimeters]

Part Weight per unit (gram): 0.0774





- 1. STANDARD LEAD FINISH: 200 MICROINCHES / 5.08 MICRONS MINIMUM LEAD / TIN (SOLDER) ON COPPER.
- SO 0.150 WIDE 8 LEADS
- 2) THESE DIMENSIONS DO NOT INCLUDE MOLD FLASH
- 3.) MAXIMUM LEAD 0.024 [0.609]



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